IN THE SPECIFICATION

Please replace the paragraph beginning on page 1, line 6, with the following replacement paragraph:

The present application is related to U.S. Patent Application Attorney Docket No. R. Nagarajan 12Serial No. 09/587,892, filed concurrently herewith in the name of inventor R. Nagarajan and entitled "Efficient Architectures for Protection Against Network Failures," which is incorporated by reference herein.

Please replace the paragraph beginning on page 3, line 1, with the following replacement paragraph:

The "bridge and select" approach illustrated in FIG. 2 provides complete redundancy for the capacity required to route the two units of OC-x traffic between nodes A and Z. However, this approach suffers from a number of significant drawbacks. For example, although the approach provides link protection, i.e., protection against a failure in one of the primary links 102 or 112, its it fails to provide span protection, where span protection refers generally to an ability to switch locally from a primary trunk to a backup trunk. In addition, since a full unit of the OC-x traffic is assigned to each link, this approach does not accommodate preemptible traffic, and fails to provide any opportunity for quality of service (QoS) enhancement.

Please replace the paragraph beginning on page 9, line 15, with the following rewritten paragraph:

The controller 210 includes a processor 220 and a memory 222. The processor 220 may be, e.g., a microprocessor, a microcontroller, an application-specific integrated circuit (ASIC) or other type of processing devicesdevice, as well as portions or combinations of such devices. The memory 222 may include an electronic random access memory (RAM), a read-only memory (ROM) or other type or memory device, as well as portions or combinations of such devices. The memory 222 may

be used to store a demand database for storing demands for network capacity, and a set of routing tables which specify routing paths through a corresponding network for particular demands.